# A Step Towards Explicit Sync

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## dma\_fence

- Thing that signals the completion of some work
  - $\circ$   $\,$  Can be waited upon, signalled etc.

Userspace API:

- Syncobj
  - Container of zero or one dma\_fence which can be replaced/reset
- Sync fd
  - File descriptor pointing to a dma\_fence

# How did implicit sync work?

- Each buffer has a dma\_resv
- dma\_resv:
  - Contains up to 1 "exclusive" fence
  - Contains up to many "shared" fences

On submit:

- Pass a list of referenced buffers
- Wait on fences from dma\_resvs.
- Put new fence in dma\_resvs.

## Iteration 1: Ignoring implicit sync in a process

The hardware has different queues that can run in parallel, e.g.

- compute
- graphics
- DMA

So let everything take only a shared reservation, and then let the driver determine things using a fence owner (=VM):

• Fences with the same fence owner are ignored

#### Problem: VR compositor

- Needed cross-process Vulkan<->Vulkan sharing
- And implicit sync interfered with some of the high priority compute

#### Iteration 2: excluding buffers from implicit synchronization

- Mark a buffer as "EXPLICIT\_SYNC"
- All submissions only wait on memory fences in the reservation
- They still add the usual fences

#### Alternative Iteration 2: Don't touch the dma\_resv

- Some driver just let you not touch the dma\_resv
- Requires some aggressive idling if you want to move buffers

## Implicit sync remainder in radv+amdgpu

- WSI buffers
  - Including Vulkan compositors importing buffers
- VA map/unmap
  - Have a shared dma\_resv for non-inter-process buffers making this hard

Have option to remove now.

## Extended dma\_resv

More categories for fences:

- 1. DMA\_RESV\_USAGE\_KERNEL
- 2. DMA\_RESV\_USAGE\_WRITE
- 3. DMA\_RESV\_USAGE\_READ
- 4. DMA\_RESV\_USAGE\_BOOKKEEP

#### Disable implicit sync everywhere!

- Only wait for KERNEL fences and set BOOKKEEP fences
  - Add a flag to submission (or context/queue/engine)

- Can stop marking buffers EXPLICIT\_SYNC.
- Except did we just break most WSI?

#### Transferring fences to/from dma\_resv

New IOCTLs:

- DMA\_BUF\_IOCTL\_EXPORT\_SYNC\_FILE
- DMA\_BUF\_IOCTL\_IMPORT\_SYNC\_FILE

Maintain buffer associated dependencies explicitly.

## Thank you to

- Christian König
- Jason Ekstrand
- Daniel Vetter