

# Rusticl

OpenCL implemented in Rust

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## State of OpenCL in Mesa - Clover

- Accepted in 2012 into Mesa
- Written in C++
- Targets LLVM (AMD) or NIR
- some development still happening (around 60 patches/year)
- Behaves differently compared to st/mesa
  - *PIPE\_SHADER\_IR\_NIR\_SERIALIZED*
  - Dynamic pipelader (only user)
  - *pipe\_compute\_state::req\_local\_mem*
  - *pipe\_grid\_info::input* for kernel inputs
- Few users, poor support in projects using CL

## Motivation

- Rust sounded like a lot of fun
- Is Rust inside Mesa feasible and sustainable?
- Is implementing existing C APIs possible in Rust?
- What are the challenges working with C bindings?
- There are actually a lot of people asking about OpenCL.

## Why use Rust?

- If the compiler complains you are doing it wrong, period
- Very pleasant developing experience
- Enforces Thread and Memory Safety by design
- The threading API is a joy

# Rusticl

- Merged into Mesa in Sept. 2022
- A modern OpenCL 3.0 gallium frontend written in Rust
- Entirely SPIR-V and NIR based
- Tries to be as close as possible to st/mesa
- Low API overhead
- Images are supported!
- OpenCL conformant (Intel 12th gen and radeonsi)
- Worker thread architecture, thread private pipe\_context

# Meson

- Nice integration into existing build system
- Rust support good enough
- No Third-party Crate support yet

## Supported Drivers

- Iris
- llvmpipe
- panfrost (WIP?)
- radeonsi (WIP)
- nouveau (WIP)
- What's the minimum level of support to enable it by default?

## Supported Software

- Darktable
- Gimp
- Anything not using OpenCL extensions.. probably



## What is required from drivers

- Add it inside *src/gallium/targets/rusticl/meson.build*
- OpenGL Compute (*PIPE\_CAP\_COMPUTE*)
- *PIPE\_SHADER\_IR\_NIR*
- some *PIPE\_COMPUTE\_CAPs*
  - *ADDRESS\_BITS*
  - *MAX\_GLOBAL\_SIZE*
  - *MAX\_MEM\_ALLOC\_SIZE*
  - *SUBGROUP\_SIZE*
- *pipe\_context::set\_global\_binding*
- for *FULL\_PROFILE*
  - 128 textures
  - 64 images
  - 16 samplers
- Function calling in backend compilers

## What is required from drivers Pt. 2

- *load/store\_global*
- For *resource\_map* (!18793)
  - *UNSYNCHRONIZED* | *COHERENT* | *PERSISTENT*
  - on UMA: *UNSYNCHRONIZED* | *DIRECTLY*
- !18581
  - *PIPE\_CAP\_SHAREABLE\_SHADER*
  - *nir\_intrinsic\_load\_kernel\_input* loading from *cb0* or *lower\_uniforms\_to\_ubo*
  - *pipe\_grid\_info.variable\_shared\_mem*

## Issues with Bindings

- *static inline*
- unsigned instead of enum types
- bitfield enums
- forward declared OpenCL types

## Issues with other stuff

- gallium very much 32 bit specific
  - buffers can only have a 31 bit size, 2GB
  - ...
- *pipe\_transfer* can't be wrapped nicely
- OpenCLs memory API
- IDE integration

## TODOs

- Reduction of Kernel launch overhead !18581
- Proper implementation of *clGetKernelWorkGroupInfo*
- Improve utilization of the GPU
- Emitting less fences
- More API Validation
- More optional OpenCL features and extensions
- Function calling
- Make OpenCL "just work"
- maybe SyCL?

## Q&A

- Any questions?