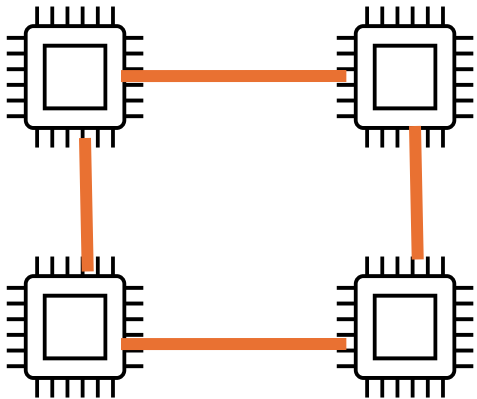


DRM Fabric: do we want a common topology API, or three more sysfs layouts?

Rodrigo Vivi - Intel

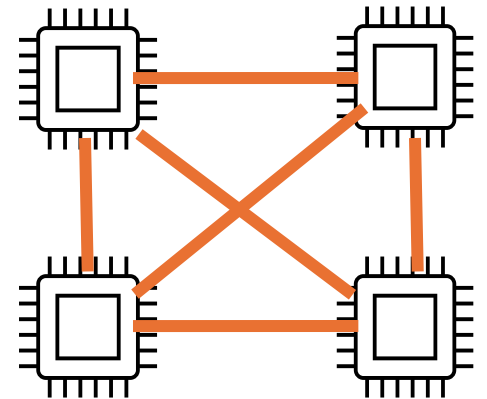
XDC - 2026



GPU to GPU

Accel to Accel

No NIC; No RDMA



We already built this three times

- AMD GPU xGMI
 - /sys/.../xgmi_* - shipping for years
- AMD UALink pods
 - New proposed pod-config sysfs
- Intel PVC XeLink / IAF
 - Netlink own genl family
 - OOT / never upstreamed – along with i915 PVC support...

Main RFC and discussions:

- Current RFC:
 - [RFC PATCH 0/12] drm/fabric: vendor-neutral topology infrastructure for scale-up accelerator interconnects
 - <https://lore.kernel.org/dri-devel/20260925121015.08b8cc15@kernel.org/T/#t>
- Plumbers scheduled discussion:
 - Oct 6th
 - <https://lpc.events/event/20/contributions/2412/>
 - Intel will be there
 - Francois Dugast, Maarten Lankhorst, Matt Brost
 - AMD confirmed online presence for discussions
 - Alex Austin, Alejandro Lucero Palau

So what? Needs and Opens

- API Options:
 - Netlink (like drm-ras)
 - Sysfs (but structured like devfreq, hwmon, etc)
 - io_uring?
- New subsystem or part of DRM?

