



# DisplayPort DRM Framework - Takeaway

Dmitry Baryshkov

Engineer, Senior Staff, Qualcomm Technologies Inc.

[Dmitry.Baryshkov@oss.qualcomm.com](mailto:Dmitry.Baryshkov@oss.qualcomm.com)

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# DisplayPort DRM Framework

## Notes from the “hacking” session

- Yes!
- First implementation targets DP 1.4a (no UBR), SST-only. MST is the next goal.
- Use `drm_connector_hdmi` as a role model
- Support link training, link retraining.
- Make **DRM\_LINK\_STATUS\_BAD** cause retrain, lowering the max link rate.
- Use BAD link status handling as a “design validation” for the framework
- SDP Frames, try to send them by default for as much as possible
- Support eDP PSR. Don't send VSC SDP with PSR commands until there is a driver which needs it
- Compliance testing pushed into helpers and mostly to the userspace
  - kernel per se is not DP compliant
  - Teach VKMS to generate **DRM\_LINK\_STATUS\_BAD**

# Thank you

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