



DisplayPort DRM Framework?

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DisplayPort drivers (as of drm-next for 6.18)

	Link Training	AUX-less training	LTPR support	eDP	eDP link_rate parsing	HDMI codec ops
AMDGPU	+		+	+	+	
GMA500	+			+		
HiBMC	+					
I915	+		+	+	+	
Nouveau			+	+	+	
Radeon	+			+		
Analogix DP core	+	+		+		
Cadence	+					
It6505	+					+
tc358767				+		
Mediatek	+			+		+
MSM	+		+	+		+
Tegra	+	+		+	+	
SN65DSI86				+	+	
Synopsis	+	+				
Xilinx	+					

References

- DRM HDMI Connector framework
- Older DRM link helpers moved to Tegra driver
- Synopsis DP bridge driver
- Standards...

Thank you

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