

# ⌘ The Occult / the Apple GPU

Alyssa Rosenzweig



# ⌘ Introduction

# ⌘ The wizards

- Dougall Johnson
- Hector Martin
- Sven Peter
- Alyssa Rosenzweig

*Designed by Asahi in Canada.*

*Assembled in... also Canada.* 

⌘ DCP

# ⌘ Hardware

- Diabolical Clusterpuck

...Er, wait.

# ⌘ Hardware

- Display Coprocessor
- Manages the display controller
- Has its own cursed coprocessor
- 7 megabytes of firmware

⌘ DCP



# ⌘ RTKit

- Real Time Kit
- Secret real-time operating system
- Apple firmware (and AirPods)
- Shared memory and mailbox

# ⌘ Firmware

- Object-oriented C++
- Remote procedure calls
- Unstable ABI ⇒ maintenance nightmare

# ⌘ Linux

- Goofy DRM/KMS driver
- Atomic KMS → DCP calls
- Haunted by IOSurface



# ⌘ Status

The screenshot shows a desktop environment with a dark theme. On the left, a MATE Terminal window is open, displaying the output of the `neofetch` command. The terminal output includes system information such as the OS (Debian GNU/Linux 11), Host (Apple Mac mini (M1, 2020)), Kernel (5.14.0-next-20210903m1+), Uptime (4 mins), Packages (1905), Shell (bash 5.1.4), Resolution (3840x2160), DE (GNOME 3.38.4), WM (Mutter), WM Theme (Adwaiata), Theme (Arc-Dark [GTK2/3]), Icons (Adwaiata [GTK2/3]), Terminal (mate-terminal), Terminal Font (Monospace 17), CPU (8), and Memory (2197MiB / 7510MiB). Below the terminal is a small color calibration bar with red, green, blue, magenta, cyan, and white squares. At the bottom of the terminal window, the word "debian" is visible.

**X.Org Developer Conference 2021**

**Overview**

**September 15-17 | Virtual**

Details on how to join us are available in [Attending XDC 2021](#).

The X.Org Developers Conference (XDC) is the event for developing graphics (Linux kernel, Mesa, DRM, Wayland, X11, etc.).

The **schedule timezone** of the conference is UTC+2, unless you change it in your user preferences along with your current timezone.

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**Contact**

To contact the organization team, send an email to:

- Radoslaw Szwichtenberg [radoslaw.szwichtenberg@intel.com](mailto:radoslaw.szwichtenberg@intel.com)

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⌘ AGX

# ⌘ Hardware

- Apple Graphics
- Tiler
- Dual-issue, scalar instruction set
- Made for Metal

# ⌘ Mesa

- Gallium3D driver
- NIR compiler

# ⌘ Metal lacks OpenGL features



# ⌘ “Fun” with AGX

# ⌘ Divergence styles

- Mali: Branches with hardware reconvergence
- AMD: Compiler manages execution masks
- Apple: Count control flow nesting

# ⌘ AGX divergence



# ⌘ AGX control flow

- 32 threads in a warp
- Implicit execution 32-bit mask
- Nesting counter in `r0l` (0 if active)
- Warp-static jumps
- Structured if, else, do...while
- Control flow sets `r0l` and mask

# ⌘ NIR control flow

- If-else
- Infinite loop
- Break, continue

# ⌘ Implementing if

```
if_icmp cond != 0 (n = 1)
    ...
pop_exec (n = 1)
```

# ⌘ Implementing if...else

```
if_icmp cond != 0 (n = 1)
    ...
else_icmp cond == 0 (n = 1)
    ...
pop_exec (n = 1)
```

# ⌘ What about loops?



# ⌘ Implementing loops, take 0

No way to break!

```
start:  
    ...  
    jmp_exec_any start
```

# ⌘ Implementing loops, take 1

```
start:  
    ...  
    do_while true (n = 1)  
    jmp_exec_any start
```

Break:

```
mov r0l, #1  
pop_exec (n = 0)
```

# ⌘ Implementing loops, take 2

Don't clobber the execution mask.

```
push_exec (n = 1)

start:
    ...
    do_while true (n = 1)
    jmp_exec_any start

pop_exec (n = 1)
```

# ⌘ Implementing loops, take 2

- Implements `loop { ... }`
- Can break out of multiple loops at once
- What about continue?

# ⌘ Implementing loops, take 3

Quoth the Dougall:

*“Continue is a break.”*

```
do {  
    do {  
        ...  
    } while(0);  
} while (cond);
```

# ⌘ Implementing loops, take 3

Model two nested loops in general.

```
push_exec (n = 2)

start:
    ...
    do_while true (n = 2)
    jmp_exec_any start

pop_exec (n = 2)
```

# ⌘ Implementing loops, take 3

Break:

```
mov r0l, #2  
pop_exec (n = 0)
```

Continue:

```
mov r0l, #1  
pop_exec (n = 0)
```

# ⌘ Implementing loops, take 4

Break:

```
mov r0l, #(nested_if_count + 2)  
pop_exec (n = 0)
```

Continue:

```
mov r0l, #(nested_if_count + 1)  
pop_exec (n = 0)
```

# ⌘ Conclusion

# ⌘ Status

- DCP driver downstream
- AGX upstream in Mesa
  - Passing 95% of dEQP-GLES2
- AGX kernel driver pending

⌘ Thank you

Alyssa Rosenzweig  
[alyssa@rosenzweig.io](mailto:alyssa@rosenzweig.io)